

Aula Riunioni- DII, via G. Caruso 16, Pisa
May, 31 – h 15.00

Multi-Phase TDC Inspired by Early Vision Model

**Takeshi Shima, Professor at
Kanagawa University, Yokohama, Japan**

Bio of Prof. Takeshi Shima

Prof. Takeshi Shima was born in Tokyo in Jan. 1951. He received the bachelor, master, and doctor degree in electrical engineering from Tohoku University in 1974, 1976, and 1986, respectively. In 1976, he joined the Toshiba R&D center, where he developed PLL TV tuning ECL-III IC, three dimensional table look-up model for SPICE, and analog neural network chips. Since 1998, he has been the professor at the Kanagawa University, Yokohama, Japan. His current major research interest is the low voltage analog CMOS circuit design. He is a senior member of IEEE, a member of IEICE and IEEJ.

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